

HIGH VOLTAGE TOLERANT DIFFERENTIAL INPUT RECEIVER

ABSTRACT OF THE DISCLOSURE

A computer system includes at least one high-voltage device and at least one low-voltage device. The low-voltage device has at least one differential input receiver, which reduces the risk of gate oxide stress by ensuring the voltage level on the low-voltage device gate(s) is at or below a predetermined value. The high-voltage device may be a memory controller and the low-voltage device may be a memory.